



#5/A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Timothy B. Cowles and
Christian N. Mohr

Serial No.: 09/864,682

Filed: May 24, 2001

Title: CIRCUIT AND METHOD FOR TEST AND
REPAIR

§
§ Group Art Unit: 2184
§
§ Examiner:
§
§ Atty. Docket: 2000-0058.01
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PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Certificate of Mailing (37 C.F.R. § 1.8)	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on the date below:	
9/12/11	Charles Brantley
Date	Signature

Please amend the above captioned application as follows.

IN THE CLAIMS

Please add the following claims.

- Sub B2
R1
97. (New) A method of handling a memory device, comprising:
testing for at least one bad cell in said memory device;
storing on said memory device a partial address of a final bad cell identified during said testing act; and
incorporating said memory device into a computer system.